

Notice of References Cited

Application/Control No.

10/599,593

Applicant(s)/Patent Under
Reexamination
DUTT ET AL.

Examiner

JUE WANG

Art Unit

2193

Page 1 of 1

U.S. PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Name | Classification |
|---|---|--|-----------------|---------------------|----------------|
| * | A | US-4,794,522 | 12-1988 | Simpson, Richard O. | 703/26 |
| * | B | US-6,477,683 B1 | 11-2002 | Killian et al. | 716/106 |
| * | C | US-7,058,932 B1 | 06-2006 | Jennings et al. | 717/138 |
| * | D | US-7,107,580 B2 | 09-2006 | Zemach et al. | 717/136 |
| * | E | US-2003/0217248 A1 | 11-2003 | Nohl et al. | 712/208 |
| * | F | US-2005/0102493 A1 | 05-2005 | DeWitt et al. | 712/227 |
| * | G | US-2005/0160402 A1 | 07-2005 | Wang et al. | 717/114 |
| | H | US- | | | |
| | I | US- | | | |
| | J | US- | | | |
| | K | US- | | | |
| | L | US- | | | |
| | M | US- | | | |

FOREIGN PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Country | Name | Classification |
|---|---|--|-----------------|---------|------|----------------|
| | N | | | | | |
| | O | | | | | |
| | P | | | | | |
| | Q | | | | | |
| | R | | | | | |
| | S | | | | | |
| | T | | | | | |

NON-PATENT DOCUMENTS

| * | | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages) |
|---|---|---|
| | U | Diep et al. "WMW: A Visualization-Based Microarchitecture Workbench", 1995, Computer, volume 28, issue 2. |
| | V | V. Rajesh, "A Generic Approach to Performance Modeling and Its Application to Simulator Generator", August 1998, Masters Thesis, Department of Computer Science & Engineering, Indian Institute of Technology Kanpur. |
| | W | Leupers et al. "Generation of Interpretive and Compiled Instruction Set Simulators" , 1999, Proceedings of the ASP-DAC '99. |
| | X | Amicel et al. "Maturing Startup costs in Assembler-Based Compiled Instruction-Set Simulation", 2002, Proceedings of the Sixth Annual Workshop on Interaction between Compilers and Computer Architectures. |

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.